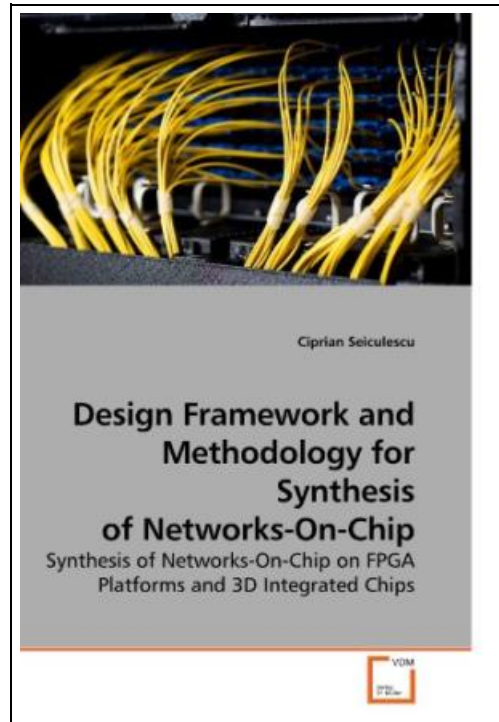# Design Framework and Methodology for Synthesis of Networks-On-Chip

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DESIGN FRAMEWORK AND METHODOLOGY FOR SYNTHESIS OF NETWORKS-ON-CHIP

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