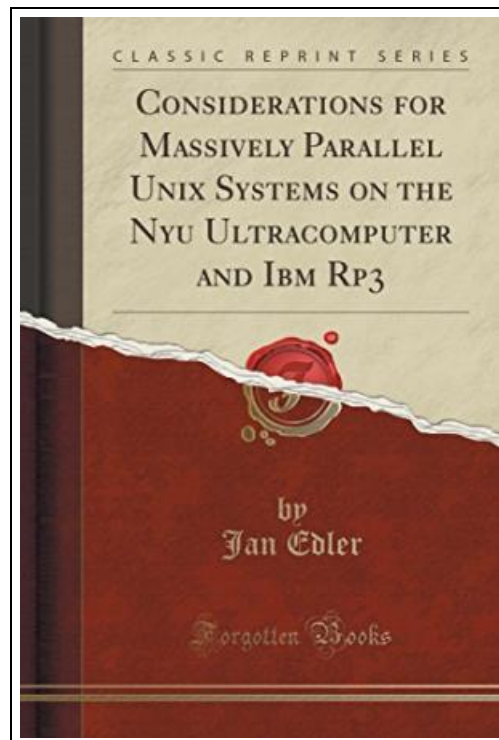


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Forgotten Books, United States, 2016. Paperback. Condition: New. Language: English . Brand New Book ***** Print on Demand *****. Excerpt from Considerations for Massively Parallel Unix Systems on the Nyu Ultracomputer and Ibm Rp3 When concurrent loads, stores, and fetch-and-adds are directed at the same memory location and meet at a switch, they can be combined without introducing any delay (see Klappholz and Gottlieb et al. Since combined requests can themselves be combined, any number of concurrent memory references to the same location can be satisfied in the time required for one central memory access. It is this property that permits the bottleneck-free implementation of many coordination protocols. The impact of network latency on performance is reduced by associating a local cache memory with each pe. Frequently-used program code and data can be accessed in (approximately) a single processor cycle when resident in the cache. Thus the network latency is eliminated from many memory accesses, and all accesses benefit from the reduced network traffic. About the Publisher Forgotten Books publishes hundreds of thousands of rare and classic books. Find more at This book is a reproduction of an important historical work. Forgotten Books uses state-of-the-art technology to digitally reconstruct the work, preserving the original format whilst repairing imperfections present in the aged copy. In rare cases, an imperfection in the original, such as a blemish or missing page, may be replicated in our edition. We do, however, repair the vast majority of imperfections successfully; any imperfections that remain are intentionally left to preserve the state of such historical works.



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